

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A multiple power source semiconductor integrated circuit including:

a plurality of ~~plural~~ function blocks that are supplied with power from a different one of a plurality of power supply circuits, respectively;

a microcomputer for controlling the supply of power to ~~the plural~~ said plurality of function blocks, said microcomputer being one of said ~~plural~~ plurality of function blocks; and

a power supply control circuit for controlling the supply of power by the power supply circuits under the control of ~~the said~~ microcomputer.

2. (Currently Amended) The multiple power source semiconductor integrated circuit as defined in Claim 1, wherein ~~the said~~ power supply control circuit ~~halts~~ is operable to halt the supply of power to ~~the said~~ microcomputer by the power supply circuit which supplies power to said microcomputer when receiving said power supply control circuit receives predetermined data from ~~the said~~ microcomputer, and ~~restarts~~ restart the supply of power to ~~the said~~ microcomputer by the power supply circuit which supplies power to said microcomputer when receiving said power supply control circuit receives an external interrupt signal ~~from outside~~.

3. (Currently Amended) The multiple power source semiconductor integrated circuit as defined in Claim 1, wherein:

~~the said~~ power supply control circuit includes a register for storing ~~the an~~ externally received interrupt signal; and

~~the said~~ microcomputer ~~detects~~ is operable to detect contents of the interrupt signal that is stored in ~~the said~~ register, after restart of the supply of power.

4. (Currently Amended) The multiple power source semiconductor integrated circuit as defined in Claim 1, wherein:

~~the said~~ power supply control circuit ~~outputs-is operable to output~~ a power cutoff signal to the power supply circuits when the supply of power by the ~~plural-plurality of~~ power supply circuits is to be halted; and

~~the said plurality of~~ function blocks and ~~the said~~ power supply control circuit each include an inter-block signal fixing circuit for fixing an input logic from a circuit to which supply of power is halted, at a "L" or "H" level in accordance with the power cutoff signal.

5. (Currently Amended) The multiple power source semiconductor integrated circuit as defined in Claim 1, wherein:

~~the said~~ power supply control circuit ~~outputs-is operable to output~~ a power cutoff signal to the power supply circuits when the supply of power by the ~~plural-plurality of~~ power supply circuits is to be stopped; and

~~the said plurality of~~ function blocks and ~~the said~~ power supply control circuit each include an inter-block signal fixing circuit for fixing an output logic to a circuit that is in a state where the supply of power is halted, at a "L" level in accordance with the power cutoff signal.

6. (Currently Amended) The multiple power source semiconductor integrated circuit as defined in Claim 1, further including: a storage ~~means-unit~~ which is always supplied with power and ~~retains-operable to retain~~ system information while the supply of power to the respective function blocks is halted.

7. (Currently Amended) The multiple power source semiconductor integrated circuit as defined in Claim 1, further including: an input/output terminal circuit for giving and receiving a signal to/from outside said input/output terminal, and

wherein said ~~the~~ power supply control circuit and ~~the said~~ input/output terminal operate on power that is supplied from a common power supply circuit.

8. (Currently Amended) The multiple power source semiconductor integrated circuit as defined in Claim 1, wherein ~~the said~~ power supply control circuit operates on

power that is supplied to the ~~plural~~plurality of power supply circuits, and ~~outputs is~~
operable to output an all power cutoff signal for stopping the supply of power by all of
the ~~plural~~plurality of power supply circuits.

9. (New) The multiple power source semiconductor integrated circuit as defined in Claim 2, wherein:

said power supply circuit control includes a register for storing the interrupt signal; and

said microcomputer is operable to detect contents of the interrupt signal that is stored in said register, after restart of the supply of power.

10. (New) The multiple power source semiconductor integrated circuit as defined in Claim 1, wherein said power supply circuit is operable to control the supply of power by the plurality of power supply circuits to each of said plurality of function blocks so as to reduce leakage current to each of said plurality of function blocks that is not being used.